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#1      (( equalizer<in>metadata ) <and> ( clock generator<in>metadata ) <and> ( oversampler<in>metadata ) )

#2      (( equalizer<in>metadata ) <and> ( clock generator<in>metadata ) <and> ( multiplexer<in>metadata ) )

#3      (( equalizer<in>metadata ) <and> ( clock synthesizer<in>metadata ) <and> ( multiplexer<in>metadata ) )

#4      (( equalizer<in>metadata ) <and> ( clock synthesizing<in>metadata ) <and> ( multiplexer<in>metadata ) )

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Day : Thursday

Date: 2/1/2007

Time: 12:02:00

**PALM INTRANET****Inventor Name Search Result**

Your Search was:

Last Name = PARK

First Name = HONG-JUNE

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">08626817</a>	<a href="#">5729178</a>	250	04/03/1996	FULLY DIFFERENTIAL FOLDED CASCODE CMOS OPERATIONAL AMPLIFIER HAVING ADAPTIVE BIASING AND COMMON MODE FEEDBACK CIRCUITS	PARK, HONG-JUNE
<a href="#">08891153</a>	<a href="#">5929654</a>	250	07/10/1997	TEMPERATURE-INSENSITIVE CURRENT CONTROLLED CMOS OUTPUT DRIVER	PARK, HONG-JUNE
<a href="#">08891154</a>	<a href="#">5880625</a>	150	07/10/1997	TEMPERATURE INSENSITIVE CONSTANT CURRENT GENERATOR	PARK, HONG-JUNE
<a href="#">09545461</a>	<a href="#">6275066</a>	150	04/07/2000	Current-mode bidirectional input/output buffer for impedance matching	PARK, HONG-JUNE
<a href="#">09547863</a>	Not Issued	161	04/12/2000	Current sense amplifier-based CMOS flip-flop of enhanced output transition speed	PARK, HONG-JUNE
<a href="#">09726413</a>	<a href="#">6356501</a>	150	12/01/2000	APPARATUS FOR GENERATING HIGH VOLTAGE SIGNAL	PARK, HONG-JUNE
<a href="#">09832869</a>	<a href="#">6816877</a>	150	04/12/2001	APPARATUS FOR DIGITAL MULTIPLICATION USING REDUNDANT BINARY ARITHMETIC	PARK, HONG-JUNE
<a href="#">09950817</a>	<a href="#">6504407</a>	150	09/13/2001	PROGRAMMABLE HIGH SPEED FREQUENCY DIVIDER	PARK, HONG-JUNE
<a href="#">10347795</a>	<a href="#">6803791</a>	150	01/22/2003	EQUALIZING RECEIVER WITH DATA TO CLOCK SKEW COMPENSATION	PARK, HONG-JUNE
<a href="#">10667658</a>	Not Issued	77	09/23/2003	Look-ahead decision feedback equalizing receiver	PARK, HONG-JUNE
<a href="#">11020771</a>	Not	41	12/21/2004	Slew rate controlled output driver	PARK, HONG-

	Issued			for use in semiconductor device	JUNE
11451921	Not Issued	30	06/13/2006	Bubble error rejecter in data converter	PARK, HONG-JUNE

Inventor Search Completed: No Records to Display.

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Day : Thursday

**PALM INTRANET**

Date: 2/1/2007

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**Inventor Name Search Result**

Your Search was:

Last Name = SOHN

First Name = YOUNG-SOO

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#"><u>10347795</u></a>	<a href="#"><u>6803791</u></a>	150	01/22/2003	EQUALIZING RECEIVER WITH DATA TO CLOCK SKEW COMPENSATION	SOHN, YOUNG-SOO
<a href="#"><u>10667658</u></a>	Not Issued	77	09/23/2003	Look-ahead decision feedback equalizing receiver	SOHN, YOUNG-SOO
<a href="#"><u>10804530</u></a>	<a href="#"><u>7143303</u></a>	150	03/19/2004	MEMORY DEVICE FOR COMPENSATING FOR A CLOCK SKEW CAUSING A CENTERING ERROR AND A METHOD OF COMPENSATING FOR THE CLOCK SKEW	SOHN, YOUNG-SOO
<a href="#"><u>10947576</u></a>	Not Issued	71	09/22/2004	Fully integrated protein lab-on-a-chip with smart microfluidics for spot array generation	SOHN, YOUNG-SOO
<a href="#"><u>11040808</u></a>	Not Issued	30	01/21/2005	Decision feedback equalization input buffer	SOHN, YOUNG-SOO
<a href="#"><u>11082497</u></a>	<a href="#"><u>7135900</u></a>	150	03/17/2005	PHASE LOCKED LOOP WITH ADAPTIVE LOOP BANDWIDTH	SOHN, YOUNG-SOO
<a href="#"><u>11259545</u></a>	Not Issued	30	10/25/2005	Phase and delay locked loops and semiconductor memory device having the same	SOHN, YOUNG-SOO
<a href="#"><u>11345451</u></a>	Not Issued	30	02/02/2006	Receiving apparatus and method thereof	SOHN, YOUNG-SOO
<a href="#"><u>11350868</u></a>	Not Issued	30	02/10/2006	Charge pump circuit for semiconductor memory device	SOHN, YOUNG-SOO
<a href="#"><u>11487545</u></a>	Not Issued	30	07/14/2006	Process-insensitive self-biasing phase locked loop circuit and self-biasing method thereof	SOHN, YOUNG-SOO
<a href="#"><u>11552102</u></a>	Not Issued	25	10/23/2006	SENSE AMPLIFIER FLIP FLOP	SOHN, YOUNG-SOO
<a href="#"><u>11561765</u></a>	Not Issued	19	11/20/2006	Output Driver and Output Driving Method for Enhancing Initial	SOHN, YOUNG-SOO

				Output Data Using Timing	
60179292	Not Issued	159	01/31/2000	System for transferring fluid samples through a sensor array	SOHN, YOUNG-SOO
60506321	Not Issued	159	09/26/2003	Biochip and methods for fabricating and reading same	SOHN, YOUNG-SOO

Inventor Search Completed: No Records to Display.

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Last Name	First Name
<input type="text" value="SOHN"/>	<input type="text" value="YOUNG-SOO"/>
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## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	62	(equaliz\$5 or equalis\$5) and (clock near2 (generat\$3 or synthesiz\$3)) and ((over adj1 sampl\$3) same (mux or multiplex\$3 or select\$3)) and (phase near2 detect\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2007/02/01 10:53
L2	9	(clock near2 (generat\$3 or synthesiz\$3)) and ((equaliz\$5 or equalis\$5) same (over adj1 sampl\$3) same (mux or multiplex\$3 or select\$3)) and (phase near2 detect\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2007/02/01 10:57
L3	1	("2004/0076228").URPN.	USPAT	OR	ON	2007/02/01 10:53
L4	30	(clock near2 (generat\$3 or synthesiz\$3)) and ((equaliz\$5 or equalis\$5) same (over adj1 sampl\$3)) and (phase near2 detect\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2007/02/01 10:57
S1	80	(equaliz\$5 or equalis\$5) and ((clock near2 (generat\$3 or synthesiz\$3)) same (external adj1 clock)) and ((over adj1 sampl\$3) same (mux or multiplex\$3 or select\$3)) and (phase near2 detect\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/10/19 14:53
S2	32	(equaliz\$5 or equalis\$5) and ((clock near2 (generat\$3 or synthesiz\$3)) same (external adj1 clock)) and ((over adj1 sampl\$3) same (mux or multiplex\$3 or select\$3)) and (phase near2 detect\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2007/02/01 10:50
S3	33	(equaliz\$5 or equalis\$5) and ((clock near2 (generat\$3 or synthesiz\$3)) same (external adj1 clock)) and (((over adj1 sampl\$3) or over\$1sampl\$3) same (mux or multiplex\$3 or select\$3)) and (phase near2 detect\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/10/19 11:48
S4	1	(equaliz\$5 or equalis\$5) same (((over adj1 sampl\$3) or over\$1sampl\$3) and ((clock near2 (generat\$3 or synthesiz\$3)) same (external adj1 clock)) and (mux or multiplex\$3 or select\$3)) and (phase near2 detect\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/10/19 11:49

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S5	18	(equaliz\$5 or equalis\$5) same (((over adj1 samp1\$3) or over\$1sampl\$3) and ((clock near2 (generat\$3 or synthesiz\$3)) and (mux or multiplex\$3 or select\$3)) and (phase near2 detect\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/10/19 11:58
S6	11	(equaliz\$5 or equalis\$5) with (((over adj1 samp1\$3) or over\$1sampl\$3) same (mux or multiplex\$3 or select\$3)) and (phase near2 detect\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/10/19 14:53
S7	3648	(clock near2 (generat\$3 or synthesiz\$3)) with (phase near2 detect\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/10/19 12:05
S8	40	(clock near2 (generat\$3 or synthesiz\$3)) with (phase near2 detect\$3) same (over\$1sampl\$3 or (over adj1 samp1\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/10/19 13:45
S9	21	(equaliz\$5 or equalis\$5) same ((over\$1sampl\$3 or (over adj1 samp1\$3)) same (mux or multiplex\$3 or select\$3)) and (phase near2 detect\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/10/19 13:59
S10	1	("4972430").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/10/19 14:00
S11	1	("6396329").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/10/19 14:03
S12	1	("20030195645").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/10/19 14:07
S13	1421	((oversampl\$3) or (over adj1 samp1\$3)) with (mux or multiplex\$3 or select\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/10/19 14:08
S14	34	((oversampl\$3) or (over adj1 samp1\$3)) with (mux or multiplex\$3 or select\$3) same (phase near2 detect\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/10/20 11:28
S15	260	(equaliz\$5 or equalis\$5) with ((over adj1 samp1\$3) or over\$1sampl\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/10/19 14:54

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S16	260	(equaliz\$5 or equalis\$5) with ((over adj1 samp1\$3) or over\$1samp1\$3 or (samp1\$3 near2 plurality near2 clock\$1))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/10/19 14:55
S17	37	(equaliz\$5 or equalis\$5) with ((over adj1 samp1\$3) or over\$1samp1\$3 or (samp1\$3 near2 plurality near2 clock\$1)) same (mux or multiplex\$3 or select\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/10/19 15:27
S18	37	(equaliz\$5 or equalis\$5 or dfe) with ((over adj1 samp1\$3) or over\$1samp1\$3 or (samp1\$3 near2 plurality near2 clock\$1)) same (mux or multiplex\$3 or select\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/10/23 15:47
S19	10	(equaliz\$5 or equalis\$5 or dfe) with ((over adj1 samp1\$3) or over\$1samp1\$3 or (samp1\$3 near2 plurality near2 clock\$1)) same (phase near2 detect\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/10/19 15:03
S20	1	("5692011").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/10/19 15:03
S21	1	("6671314").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/10/19 15:04
S22	1	("20040032905").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/10/19 15:06
S23	29187	(decision adj1 feed\$1back adj1 equaliz\$3) and (oversamp1\$3) and (phase adj1 detect\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/10/19 15:07
S24	29	(decision adj1 feed\$1back adj1 equaliz\$3) and (oversamp1\$3) and (phase adj1 detect\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/10/19 15:07
S25	40	((phase near2 detect\$3) or (timing near2 decision\$1)) with (clock near2 (synthesiz\$3 or generat\$3)) same (over\$1samp1\$3 or (over adj1 samp1\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/10/19 16:25
S26	1	("20030195645").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/10/19 16:25

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S27	63	((oversamp1\$3) or (over adj1 samp1\$3)) with (mux or multiplex\$3 or select\$3) same ((phase near2 detect\$3) or pll)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/10/20 11:29
S28	22	((oversamp1\$3) or (over adj1 samp1\$3)) adj7 (mux or multiplex\$3 or select\$3) same ((phase near2 detect\$3) or pll)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/10/20 11:37
S29	23	((phase near2 detect\$3) or pll or (phase adj1 locked adj1 loop)) near2 (decid\$3 or determin\$4) near3 (samp1\$3 near2 clock)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/10/20 11:39
S30	0	(interleav\$3) same (bias\$2 near2 miror\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/10/20 16:39
S31	0	(bias\$2 near2 miror\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/10/20 16:39
S32	2587	(bias\$2 near2 mirror\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/10/20 16:39
S33	426	(bias\$2 adj1 mirror\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/10/20 16:39
S34	102691	(bias\$2 adj1 mirror\$3) smae (interleav\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/10/20 16:40
S35	2	(bias\$2 adj1 mirror\$3) same (interleav\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/10/20 16:40

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S36	4	375/233.ccls. and (equaliz\$5 or equalis\$5 or dfe) with ((over adj1 samp1\$3) or over\$1samp1\$3 or (samp1\$3 near2 plurality near2 clock\$1)) same (mux or multiplex\$3 or select\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/10/23 15:49
S37	21	"375"/\$.ccls. and (equaliz\$5 or equalis\$5 or dfe) with ((over adj1 samp1\$3) or over\$1samp1\$3 or (samp1\$3 near2 plurality near2 clock\$1)) same (mux or multiplex\$3 or select\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/10/23 15:52
S38	0	"326"/\$.ccls. and (equaliz\$5 or equalis\$5 or dfe) with ((over adj1 samp1\$3) or over\$1samp1\$3 or (samp1\$3 near2 plurality near2 clock\$1)) same (mux or multiplex\$3 or select\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/10/23 15:53
S39	2	"333"/\$.ccls. and (equaliz\$5 or equalis\$5 or dfe) with ((over adj1 samp1\$3) or over\$1samp1\$3 or (samp1\$3 near2 plurality near2 clock\$1)) same (mux or multiplex\$3 or select\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/10/23 15:53